

Email: sales@hongdacap.com.hk
Skype: Hongdacapacitors

Web: www.hongdacap.com.hk Tel: +86 (0)769 82207248

HSLC Series Single Layer Capacitor

1. Capacitor characteristics and applications

1. 1 Characteristics

- Size specifications are standardized and suitable for surface mount components in hybrid integrated circuits or printed circuits;
- Low ESR and high reliability;
- Low loss, high capacitance stability, high operating frequency;

1. 2 Main performance

- Insulation resistance: Class I and II ceramics≥10000 M Ω , Class III ceramics≥1,000 M Ω at 20°C
- Operating temperature: W/F/G: -55°C~125°C; X: -35°C~+85°C

2. How to order

HSLC I	<u>1111</u>	<u>C0G</u>	1 <u>R0</u>	<u>В</u> І	<u>501</u>	<u>N</u>	<u>T</u>
Туре	Size	Dielectric	Capacitance unit: (pF)	Tolerance	Rated voltage	Termination	Packaging style
HSLC series single layer capacitor	0402 2525 0603 3838 0805 6243 0505 7676 1111	C0G: +30ppm/°C	The first two digits are significant figures, and the last digit is the power of 10	B: ±0.10pF C: ±0.25pF D: ±0.50pF	The first two digits are significant figures, and the last digit is the power of 10	N: Leading-out Terminal: Ag/Ni/Sn Z: Leading-out Terminal: Ag/Ni/ SnPb E: Non-magnetic Terminal M: Microstrip A: Axial tape RW: Radial wire RN: Non- magneticradialwire	T: Tape & reel C: Cut Tray B: Bulk

3. Product Dimensions

Due do et Torre	Size	Dimensions (mm)			Size	Dimensions (mm)		
Product Type	Code	L	W	T	Code	L	W	T
P7/////// — T	10	0.254±0.076	0.254±0.076	0.20±0.06	15	0.381±0.076	0.381±0.076	0.20±0.06
Lead-out electrode	20	0.508±0.127	0.508±0.127	0.20±0.06	25	0.635±0.127	0.635±0.127	0.20±0.06
L Ceramic T	30	0.762±0.127	0.762±0.127	0.20±0.06	35	0.889±0.127	0.889±0.127	0.20±0.06
//////////////////////////////////////	40	1.016±0.127	1.016±0.127	0.20±0.06	50	1.270±0.254	1.270±0.254	0.20±0.06
Type A	70	1.780±0.254	1.780±0.254	0.20±0.06	90	2.290±0.254	2.290±0.254	0.20±0.06

Due do et True	Size	Dimensions (mm)			Size	Size Dimensions (mm)				
Product Type	Code	L	W	T	В	Code	L	W	T	В
- B	10	0.254±0.076	0.254±0.076	0.20±0.06	0.05±0.025	15	0.381±0.076	0.381±0.076	0.20±0.06	0.05±0.025
Lead-out	20	0.508±0.127	0.508±0.127	0.20±0.06	0.05±0.025	25	0.635±0.127	0.635±0.127	0.20±0.06	0.05±0.025
electrode L Ceramic T	30	0.762±0.127	0.762±0.127	0.20±0.06	0.05±0.025	35	0.889±0.127	0.889±0.127	0.20±0.06	0.05±0.025
, 555,	40	1.016±0.127	1.016±0.127	0.20±0.06	0.05±0.025	50	1.270±0.254	1.270±0.254	0.20±0.06	0.05±0.025
₩ Type B	70	1.780±0.254	1.780±0.254	0.20±0.06	0.05±0.025	90	2.290±0.254	2.290±0.254	0.20±0.06	0.05±0.025



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Product Type		Size	Dimensions (mm)				Size	Size Dimensions (mm)			
		Code	L	W	T	В	Code	L	W	T	В
В		10	0.254±0.076	0.254±0.076	0.20±0.06	0.05±0.025	15	0.381±0.076	0.381±0.076	0.20±0.06	0.05±0.025
	Lead-out		0.508±0.127	0.508±0.127	0.20±0.06	0.05±0.025	25	0.635±0.127	0.635±0.127	0.20±0.06	0.05±0.025
L	electrode Ceramic T body	30	0.762±0.127	0.762±0.127	0.20±0.06	0.05±0.025	35	0.889±0.127	0.889±0.127	0.20±0.06	0.05±0.025
	40	1.016±0.127	1.016±0.127	0.20±0.06	0.05±0.025	50	1.270±0.254	1.270±0.254	0.20±0.06	0.05±0.025	
- W	Type C	70	1.780±0.254	1.780±0.254	0.20±0.06	0.05±0.025	90	2.290±0.254	2.290±0.254	0.20±0.06	0.05±0.025

Draduat Type	Size	Dimensions (mm)						
Product Type	Code	L	W	T	G			
L - L	0201	0.508±0.127	0.254±0.051	0.20±0.06	0.102±0.051			
w	0402	1.016±0.127	0.508±0.127	0.20±0.06	0.102±0.051			
Type D	0603	1.524±0.127	0.762±0.127	0.20±0.06	0.102±0.051			
Lead-out electrode	0805	2.032±0.254	1.270±0.127	0.20±0.06	0.102±0.051			
Ceramic	1206	3.048±0.254	1.524±0.254	0.20±0.06	0.102±0.051			

Draduat Type	Size		Dimensions (mm)				
Product Type	Code	L	W	S	G	T	
L	15		0.381±0.076		0.102±0.051	0.20±0.06	
W t	20		0.508±0.127		0.102±0.051	0.20±0.06	
-G S -	25	Customization	0.635±0.127	Customization	0.102±0.051	0.20±0.06	
Lead-out electrode	30		0.762±0.127		0.102±0.051	0.20±0.06	
Ceramic body	35		0.889±0.127		0.102±0.051	0.20±0.06	

4. Technical requirements and test conditions

4.1 General specifications and detailed specifications

• General specification: GJB 2442A-2021

《General Specification for Single-Layer Chip Ceramic Capacitors with High Reliability》

• Detailed specification: Q/CT 07B-2022

《Detailed Specification for Single-Layer Chip Capacitors》

4.2 Conventional technical indicators and test methods

Item	Technical specifications		Test method				
Operating Temperature Range	(-55 ~ +125)℃						
Appearance	No obvious defects		Visu	ual inspection			
Canacitanas	Within the	Nominal capacitance	Test frequency	Test voltage	Environment		
Capacitance	tolerance of specification	≤100pF	1MHz(±10°	%) (1.0±0.2)Vrms	ge Environment 0.2)Vrms 0.2)Vrms Humidity<75% Capacitance'		
	specification	>100pF	1KHz(±10%	(6) (1.0±0.2)Vrms	Humidity<75%		
Loss Tangent	<0.025	Te	Test method: Same as 'Capacitance'				
Insulation	Class I and II ceramics≥10000 MΩ	Test voltage	Test time	Charge and discharge current	Environment		
Resistance	Class III ceramics≥1,000 MΩ	Ur	≤60 sec	≤50mA	Temperature(25±2)°C Humidity<75%		



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Dielectric Withstand	No dielectric breakdown	Rated voltage	Test voltage	Time	Charge and discharge current		
Voltage	or damage	Ur	2.5U _R	5 sec	≤50mA		
		Step	2.5U _R 5 sec ≤50mA fter the temperature is stable for 30 minutes in the imperature order (△C is based on T3) Temperature (°C) 25±2 -55±3 25±2 125±2 25±2 should be tested according to GJB 548C-2021, 11.2, using the following specifications:				
Voltage	C0G: 0±30ppm/°C	T1	25±2				
Temperature	Y5V: +22% ~ -82% X7R: +15%	T2	-55±3				
Characteristics	BD(DM): -3300±1000ppm/°C	T3	25±2				
	DB(Biii):	T4	125±2				
		T1	25±2				
Bond Strength	No cracks at the interface between the wire and the electrode, nor any separation between the dielectric and the electrode.	Capacitors should be tested according to GJB 548C-2021, Method 2011.2, using the following specifications: a) Sample Installation: Capacitors should be installed as specified; b) Test Conditions: D; c) Minimum Bond Strength: ≥6 gf.					

4.3 Reliability indicators and periodic testing methods

Item	Technical specifications			Test method					
	Appearance	No mechanical damage	Capacitors should be tested according to the following procedures:						
Solderability Change from the initial measurement value ≤ ±10% Change from the initial measurement value ≤ ±10% Change from the initial measurement value ≤ ±10% a) Sample Installation: The capacitor should be place b) Testing: Heat the hot plate with the sample to 310 c) Recovery time after testing before measurement: maximum 24 hours. d) Post-test Inspection and Measurement: Visually in appearance and measure the capacitance as specifie						O°C and hold for (5±1) Minimum 10 minutes, aspect the capacitor's			
Shear Strength	Within the specified applied force range, the capacitor should not exhibit the failure conditions specified in 4.3.1 of Method 2019.3 of GJB 548C-2021.			Capacitors should be tested according to Method 2019.3 of GJB 548C-2021, using the following specifications: a) Sample Installation: Capacitors should be installed in accordance with 4.5.1. b) Minimum Shear Strength: According to Figure 4 in GJB 548C-2021, Method 2019.3. c) Applied Force: Twice the minimum shear strength or cutting the product from a fixed position (Take the first occurring value).					
	Appearance	No visible damage	Number of cycles: 5 times, one cycle is divided into the following 4 steps:						
	С	Same as the initial standard		Step	Temperature (°C)	Time (min)			
Temperature		Same as the		Step1	-55±3	30			
Shock	D.F.	initial standard		Step2	25±3	3			
		Same as the		Step3	125±3	30			
	I.R.	initial standard		Step4	25±3	3			
	D.W.V. Same as the initial standard			100% test. After the test, place it at room temperature for 24±2 hrs before measuring again.					



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	Appearance	No visible damage	Rated voltage	Applied voltage	Test temperature:				
	С	Same as the initial standard			125°C				
Voltage Conditioning	D.F.	Same as the initial standard	U_{R}	$2U_{\text{R}}$	Test time:				
Conditioning	I.R.	Same as the initial standard			100 hours				
	D.W.V.	Same as the initial standard	100% test. After the test, place it at roon temperature for 24±2 hrs before visual i	n nspection.					
	Appearance	No corrosion or mechanical damage	Capacitors should be tested according		ethod 104, using				
	∆C/C	Change from the initial measured value: ≤±10%	the following specifications:						
	D.F.	Same as the initial standard	b) Inspection and Measurement after	, ,					
Impregnation	I.R.	Should be no less than 30% of the initial required value at 25°C.	capacitor's appearance and measure of withstand voltage, and insulation resis		gent, dielectric				
	D.W.V.	Same as the initial standard							
	Appearance	No corrosion or mechanical damage	Capacitors should be tested according to GJB 360B-2009, Method 103, using the following specifications: a) Pre-test Measurement: Measure capacitance as specified;						
Steady State Damp Heat (Low Voltage)	∆C/C	Change from the initial measured value: ≤±10%	b) Test Conditions: Temperature: $85\pm 2^{\circ}$ C, relative humidity: $85\%\pm 5\%$, time: 2 DC voltage of $1.3V\pm 0.25V$ should be continuously applied to the capacitor th $100k\Omega$ resistor. c) Post-test inspection and measurement: Remove the capacitor from the teand allow it to dry and stabilize at 25° C for $3.5h\pm 0.5h$ (while applying a volta of $1.3V\pm 0.25V$ through a $100k\Omega$ resistor). Measure the insulation resistance (test voltage $1.3V\pm 0.25V$) and capacitance as specified, and visually inspect						
	I.R.	Should be no less than the initial required value at 25°C.	 (lest voltage 1.3v ± 0.25v) and capacitance as specified, and visually inspect the capacitor's appearance. d) During the test, the voltage applied to any capacitor under test must not exceed 1.55v. Capacitors may be equipped with leads for mounting and loading, and loading may be applied mechanically. 						
	Appearance	No mechanical damage	Capacitors should be tested according to GJB 360B-2009, Method 108, using the following specifications: a) Sample Installation: Capacitors should be installed as specified.						
	∆C/C	Change from the initial measured value: ≤±10%	Test Temperature: 125°C c) Operating Conditions: The capac rated DC voltage, and the surge curr	citor should be loaded	with twice the				
Life Test	D.F.	≤ 1.2 times the initial standard	appropriate current-limiting resistor should be connected to the circuit. d) Test Conditions: F (2000h). e) Measurements During the Test: After 1000h and at the end of the test, while the capacitor is still at the maximum operating temperature, insulation resistance should be measured as specified. f) Post-Test Inspection and Measurements: The capacitor should be restore to the specified inspection conditions. Visually inspect the capacitor's appearance, and then measure capacitance, loss tangent, and insulation resistance as specified.						
	I.R.	Should be no less than 30% of the initial required value at 25°C							

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5. Precautions for use

1. Precautions before use

In harsh operating environments or under external mechanical overpressure that exceed the operating conditions described it, single-layer capacitors may be damaged. Therefore, when consider applying according to the relevant instructions in this approval using.

2. Wire Bonding

2.1 If wire bonding is used, it is recommended that the maximum temperature not exceed 320°C in a nitrogen atmosphere, and that the bonding time not exceed 10 seconds in environments above 300°C.

2.2 Wire Connection:

Wire Type: Gold wire (25 µm)

It is recommended that the bonding area not be located at the edge of the electrode, but as close to the center as possible; It is recommended to use thermocompression bonding or ultrasonic ball bonding and the temperature should be maintained between 100°C and 120°C.

2.3 Reliability may be reduced if the capacitor is exposed to rapid thermal shock.

(For example: heating: 2-5°C/second; cooling: 1-4°C/second)

3. Issues to consider for automatic installation

If the suction pipe is lowered beyond the minimum limit, it will exert excessive pressure on the Single Layer Capacitor and cause SLCC to rupture.

When lowering the suction pipe, pay attention to the following points:

- 3.1 After correcting the deviation of the load board, adjust the lower limit of the suction pipe to the surface level of the load board.
- 3.2 It is recommended to adjust the pickup pressure to a value that just barely picks up the capacitor.

4. Welding

- 4.1 This product features a four-layer metal system (TaN/TiW/Ni/Au) on the surface. This metal system is suitable for Welding with conductive adhesives.
- 4.2 Single-layer capacitors are a combination of ceramic and metal. Metal has good thermoplasticity and responds quickly to heat, while ceramic has poor thermoplasticity and responds more slowly to heat. Therefore, under heating conditions, there must be a certain degree of inconsistency in expansion between metal parts and ceramic parts, this will cause internal stress and easily cause ceramic body cracking. If soldering is to be performed at a higher temperature, It is recommended to preheat continuously for more than 1 minute before welding.

5. Cleaning

Ultrasonic cleaning is recommended for single - layer capacitors.

Ultrasonic output: less than 20W/L; Ultrasonic frequency: less than 40KHz; Ultrasonic cleaning time: 5 minutes or less

6. Storage method

In order to maintain the bonding strength of single-layer capacitors and ensure that packaging materials are in good condition, recommended storage conditions are as follows:

Storage temperature: 5 - 40°C; Storage relative humidity: 20 - 70%RH.

Even if stored under ideal storage conditions, the bonding strength of the terminal electrodes of single-layer capacitors will decrease over time.

Therefore single-layer capacitors should be used within 6 months from date of shipment.

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6. Product Packaging

6.1 Box

Spec(Specification)	Qty (Quantity)	Comment
15	400/box	The packaging form and
30	400/box	quantity can be customized

7. Inspection results of prohibited substances in products about RoHS

All products meet the requirements of the RoHS compliances:

- Lead(pb) (< 1000ppm)
- Mercury (Hg) (< 1000ppm)
- Cadmium(cd) (< 100ppm)
- Hexavalent Chromium Content(Cr6+) (< 1000ppm)
- Polybrominated Biphenyls(PBBs) (< 1000ppm)
- Polybrominated diphenyl ethers(PBDE) (< 1000ppm)